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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10083161	FILING DATE 02/27/2002	CLASS 711	SUBCLASS 111	GAU 2185	EXAMINER M. Kim
* APPLICANTS: Kimura Hiroaki; Shirahige Yuji; Yamazaki Iwao; 2186					
** CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-028022 10/25/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's Initials				ATTORNEY DOCKET NO 826.1799	
Title: Onchip control device and method					

U.S. DEPT. OF COMM. / PAT. & TM. PTO. 425L (Rev. 12-01)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
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